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INFORMATION DISCLOSURE STATEMENT				Applicants: Kaori YASUFUKU et al.	
				Filing Date: August 23, 2000	Group Art Unit: 2833
OTHER DOCUMENT (Including Author, Title, Date, Pertinent Pages, Etc.)					
Examiner					
<i>JA</i>	Inventors: Kaori Yasufuku et al., "Cap and Low Insertion Force Connector for Printed Circuit Board", Filed: August 23, 2000, Specification & Drawings of US Patent Application No. 09/643,963				
<i>JA</i>	Inventors: Kaori Yasufuku et al., "Connector for Module", Issued: August 21, 2001, US Patent Serial No. 6,278,610				
Examiner <i>Felix D. Figueroa</i>				Date Considered <i>12/18/01</i>	
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					

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